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Attorney Docket No. ~~SE 142~~ 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
TC 2800 MAIL ROOM

In Re Application of:

Hisashi OHTANI

Serial No.: 09/916,913

Filed: July 27, 2001

Art Unit: 2823

For: Method Of Manufacturing Thin
Film Transistors

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) I hereby certify that this correspondence
) is being deposited with the United States
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) Date: 2-19-02

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby call the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the documents is enclosed herewith for the Examiner's consideration.

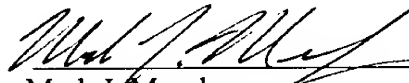
No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

Please charge our Deposit Account No. 50/1039 for the amount for this IDS.

Respectfully submitted,


Mark J. Murphy
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